

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention	A METHOD, SYSTEM, AND COMPUTER PROGRAM PRODUCT FOR AUTOMATIC INSERTION AND CORRECTNESS VERIFICATION OF LEVEL SHIFTERS IN INTEGRATED CIRCUITS WITH MULTIPLE VOLTAGE DOMAINS
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Application Number :

10-711971

Confirmation Number:

First Named Applicant:

Bhanu KAPOOR

Attorney Docket Number:

Q83911

Art Unit:

Examiner:

Search string:

(6083271 or 6614283 or 6351173).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
TD	1	6083271	2002-07-04	Morgan		/	/
TD	2	6614283	2003-09-02	Wright		/	/
TD	3	6351173	2002-02-26	Ovens		/	/

Signature

[Handwritten Signature]

Examiner Name	Date
THUAN DO	5-20-06